THIS THIRD ARTICLE IN A SERIES ON active filters focuses on audio signal processing and amplitude control circuits. The last article, in the February 1995 issue of Electronics Now, page 65, discussed popular passive control networks including high- and low-pass filtering circuits. This article picks up where that article left off with a discussion of active tone-control circuits, and it goes on to explain amplitude-regulating circuits.

Active tone controls
An active tone-control circuit can be made by connecting a passive tone-control network to the negative feedback loop of a linear amplifier, typically an operational amplifier. This circuit provides signal gain rather than attenuation.

Tone-control networks can be simplified versions of Fig. 15 in the February 1995 article. However, they are more likely to be based on the alternative passive tone-control circuit shown here as Fig. 1. This circuit’s performance is comparable, but requires fewer components, and it includes two linear control potentiometers.

If the input signals to the circuit in Fig. 1 are low enough so that capacitors C1 and C2 act like open circuits, the output signal amplitudes are controlled entirely by resistor R5. This occurs because resistor R6 is isolated from the output by capacitor C2.

However, at input frequencies that are high enough so that the two capacitors act like short circuits, the output signal amplitudes are controlled entirely by resistor R6. In this situation, resistor R5 is short circuited by C1.

The low-frequency (bass) circuit cutoff is determined by the values of R1 and C1, and the high-frequency (treble) cutoff is determined by C2 and the values of R1 to R3.

Figure 2 illustrates how the network in Fig. 1 is integrated into an active tone control circuit that can provide up to 20 decibels (dB) of boost or cut to bass or treble signals.

The circuit shown in Fig. 3, although similar to that of Fig. 2, is more versatile. It has an additional filter control network that is centered on the 1-kHz midband of the audio spectrum. This network permits the midband to be boosted or cut by as much as 20 dB.

Graphic equalizers
The more sophisticated graphic equalizer tone-control system consists of a many parallel-connected, variable-response filters that overlap and have narrow-passbands to cover the entire audio spectrum. This cir-
FIG. 3—THREE-BAND ACTIVE TONE-CONTROL control (bass, midband, treble).

FIG. 4—TYPICAL OCTAVE (GRAPHIC) equalizer section.
FIG. 5—TEN-OCTAVE (GRAPHIC) equalizer circuit.

FIG. 6—TYPICAL PHONOGRAPH record playback frequency response curve.

FIG. 7—RIAA PLAYBACK equalization curve.

which, in turn, is proportional to recording frequency.

Phono disk recording equipment usually did not provide truly linear frequency response. To enhance the effective dynamic range and signal-to-noise ratio of records, frequencies below 50 Hz and those in the 500-Hz to 2.12-kHz midband range were recorded nonlinearly in accordance with a standard curve defined by the Recording Industry Association of America (RIAA).

This nonlinearity causes a midband drop of 12 dB when the record is played through linear-response ceramic or crystal pickups, but this decrease was too small to be objectionable in most low-end record players.

Figure 6 shows a plot of a typical phonograph record playback frequency response curve as a solid line and dotted line superimposed on the solid line that represents the ideal response curve. The ideal (dotted-line) curve is flat to 50 Hz where it rises at a 6 dB/octave rate to 500 Hz. It remains flat from 500 Hz to 2120 Hz, then rises again at a 6 dB/octave rate to about 20 kHz.

When a record is played through a magnetic pickup in a high quality hi-fi system, the output of the pickup is preamplified before going to the power amplifier. The preamplifier must have a frequency equalization curve that is the exact inverse of that shown in Fig. 6, so that an overall linear response is obtained. Figure 7 shows the RIAA equalization curve. It is the inverse of recording curve shown in Fig. 6.

RIAA preamplifier

Magnetic pickup cartridges are low-sensitivity devices that give typical midband outputs of only a few millivolts. Consequently, their output signals must be preamplified by a dedicated, low-noise preamplifier integrated circuits rather than general-purpose operational amplifiers. A schematic for a preamplifier with integral RIAA magnetic-pickup equalization is shown in Fig. 8. The circuit includes an LM381 low-noise, dual preamplifier IC.
FIG. 8—LOW-NOISE MAGNETIC phono cartridge preamplifier that includes RIAA equalization.

FIG. 9—NONLINEAR (SEMILOG) amplifier based on the 741 operational amplifier.

TABLE 1
NONLINEAR AMPLIFIER PERFORMANCE

<table>
<thead>
<tr>
<th>Millivolts (input, RMS)</th>
<th>$V_{out}$ (mV RMS)</th>
<th>$V_{gain}$</th>
<th>$V_{out}$ (mV RMS)</th>
<th>$V_{gain}$</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.0</td>
<td>110</td>
<td>$\times$110</td>
<td>21</td>
<td>$\times$21</td>
</tr>
<tr>
<td>10.0</td>
<td>330</td>
<td>$\times$33</td>
<td>170</td>
<td>$\times$17</td>
</tr>
<tr>
<td>100.0</td>
<td>450</td>
<td>$\times$4.5</td>
<td>360</td>
<td>$\times$3.6</td>
</tr>
<tr>
<td>1000.0</td>
<td>560</td>
<td>$\times$0.56</td>
<td>470</td>
<td>$\times$0.47</td>
</tr>
<tr>
<td>10,000.0</td>
<td>600</td>
<td>$\times$0.07</td>
<td>560</td>
<td>$\times$0.56</td>
</tr>
</tbody>
</table>

TABLE 2
CONSTANT-VOLUME AMPLIFIER PERFORMANCE

<table>
<thead>
<tr>
<th>Millivolts (input)</th>
<th>$V_{out}$ (volts)</th>
<th>$V_{gain}$</th>
</tr>
</thead>
<tbody>
<tr>
<td>500</td>
<td>2.85</td>
<td>$\times$5</td>
</tr>
<tr>
<td>200</td>
<td>2.81</td>
<td>$\times$14</td>
</tr>
<tr>
<td>100</td>
<td>2.79</td>
<td>$\times$28</td>
</tr>
<tr>
<td>50</td>
<td>2.60</td>
<td>$\times$52</td>
</tr>
<tr>
<td>20</td>
<td>2.03</td>
<td>$\times$101</td>
</tr>
<tr>
<td>10</td>
<td>1.48</td>
<td>$\times$148</td>
</tr>
<tr>
<td>5</td>
<td>0.89</td>
<td>$\times$180</td>
</tr>
<tr>
<td>2</td>
<td>0.4</td>
<td>$\times$200</td>
</tr>
<tr>
<td>1</td>
<td>0.2</td>
<td>$\times$200</td>
</tr>
<tr>
<td>0.5</td>
<td>0.1</td>
<td>$\times$200</td>
</tr>
</tbody>
</table>

As an alternative, a National Semiconductor LM387, another low-noise, dual preamplifier will work in the circuit. Both the National LM381 and LM387 are suitable as amplifiers in audio-tape playback preamplifiers.

The pin numbers shown in Fig. 8 are those of the first half of the LM381, packaged in a 14-pin DIP. (The LM387 is packaged in an 8-pin DIP.) Because two of these preamplifier circuits are needed in a stereo audio system, both sections of the dual preamplifier ICs are used.

The LM381 has a high, open-loop gain of 112 dB, and a total input noise rating of 0.5 microvolts. Its output voltage swing equals the supply voltage minus 2 volts and it has a wide power bandwidth of 75 kHz, 20 volts peak-to-peak. The LM387 is similar to the LM381 except that it has an open-loop gain of 104 dB and a total input noise rating of 1.0 microvolt. The amplifiers in both ICs are electrically independent of each other.

The LM381 in Fig. 8 is configured as a noninverting amplifier. Negative feedback is applied from the output to the inverting input terminal. The voltage divider consisting of resistors R3 and R4, and the network formed by resistor R6 and capacitor C2 determines AC signal gain.

At the audio frequency mid-band (centered on 1 kHz) capacitors C2 and C3 have low impedances and C4 has a high impedance. As a result, AC gain is determined principally by the value of resistor R5 divided by R6, and it equals about 400. At lower frequencies, the impedance of C3 becomes significant and it causes AC gain to increase until, at very low frequencies, it is limited to 4000 by the ratio of the value of resistor R3 with respect to R6.

By contrast, at high frequencies, the impedance of C4 falls significantly, shunting R5. This causes the AC gain to decrease until, at very high frequencies, it is limited to 10 by the ratio of the values of R4 to R6. The signals form the magnetic pickup cartridge are AC coupled to the LM381 by C1.

Nonlinear amplifiers

An operational amplifier will act as a nonlinear amplifier if a nonlinear component is included in its negative feedback network. Figure 9 shows two square-law response (nonlinear) feedback elements, a pair of diodes connected back-to-back in the feedback loop.

When small signals are applied to this circuit, the diodes act as infinite resistance (open circuits), so circuit gain is high. However, when large signals are applied, the diodes act as low resistances, so circuit gain is low.

The gain follows a semilogarithmic function, and circuit sensitivity can be varied by altering the value of resistor R1. Table 1 summarizes the circuit performance with two different values of R1—1 and 10 kilohms. For example, it can be seen that a 1000:1 change in input signal amplitude causes a change as small as 2:1 in output level. This characteristic can be put to practical use in single-range bridge-balance detectors and signal-strength indicators. Voltage measurements can be made with an AC millivoltmeter.

When a sinewave input is applied to the circuit, the two diodes limit the output voltage swing to about 1.4 volts peak-to-peak by clipping the waveform. The output approximates a

Continued on page 93
The operational amplifier is configured as an AC amplifier with its gain controlled by the ratio of the values of resistor R1 with respect to R2 and by the AC voltage divider formed by R4 in series with the internal impedance of Q1. This FET functions as a voltage-controlled resistor. Its control voltage is obtained from the output of the operational amplifier with a network formed by diode D1 in series with resistor R5 and resistor R6 in parallel with capacitor C3.

When a small signal is applied to the 741, its output is small. Consequently, very little negative bias is developed on the FET gate from the network consisting of D1, R5, R6 and C3. The FET functions as a resistance with a value of several hundred ohms. The voltage divider formed from R4 and Q1 causes slight negative feedback that is applied to the 741, so it provides high voltage gain.

By contrast, if a large signal is applied to the 741, its output is large, so a large negative bias is developed on the gate of FET Q1 from the D1, R5, C3 network. As a result, Q1 acts like a very high resistance. In this condition, the R4/Q1 divider applies large negative feedback to the 741, and it provides low voltage gain.

The overall effect of this response characteristic is that the mean level of the output signal is self-regulated at 1.5 to 2.85 volts over a 50:1 range of input signal levels. This is equal to 500 to 10 millivolts. It does this without generating audible signal distortion. The circuit's performance is summarized in Table 2.

The value of resistor R1 determines the sensitivity of the circuit. It is selected to accommodate the maximum input signal amplitude that the circuit is expected to handle. This is determined on a basis of 200 kilohms per RMS volt of input signal.

For example, to accommodate a maximum input of 50 volts, R1 should have a value of 10 megohms. Capacitor C3 determines the automatic gain control time constant of the circuit.

FIG. 10—CONSTANT-VOLUME amplifier that includes a JFET.